



University Program

90nm, 32/28nm Generic Libraries and iPDKs

In addition to EDA tools and IP, universities need access to libraries and process design kits to implement a complete IC design flow. Synopsys developed the Generic Library and the Interoperable Process Design Kit (iPDK) in 90nm and 32/28nm versions to aid faculty in teaching modern microelectronic design techniques and to provide students with real-world design experiences that prepare them for careers in industry.

Generic Library

The Synopsys 90nm and 32/28nm Generic Libraries are designed for teaching and enables students to master advanced design methods such as low power. They include sample designs, as well as support for the OpenSPARC, PowerPC® 405 and Cortex M0™ processors.

Content includes:

- ▶ Digital Standard Cell Library
- ▶ I/O Standard Cell Library
- ▶ I/O Special Cell Library
- ▶ Set of Memories
- ▶ Set of Low Power Memories
- ▶ Phase Locked Loop
- ▶ Design References

Interoperable Process Design Kits

The Synopsys 90nm and a 32/28nm versions of the iPDK are also designed for teaching and enables students to master design of analog and mixed-signal ICs and IP using the latest Synopsys Custom Implementation tools.

Content includes:

- ▶ Technology Files
- ▶ Physical Verification Files
- ▶ Parasitic Extraction Files
- ▶ Symbol Library and Python PCells
- ▶ HSPICE Models
- ▶ Callback Scripts
- ▶ Setup Files

The Synopsys Generic Libraries and iPDKs are designed as teaching resources only and are not intended for fabrication. If you intend to tape out your design we recommend you use the resources available through your local wafer fabrication services company such as EuroPractice or MOSIS.

Resources may be accessed with a SolvNet ID and password from the Synopsys University Program [Members Only website](#).



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Synopsys University Program website:
www.synopsys.com/Community/UniversityProgram

Download Resources Now!

<https://www.synopsys.com/apps/protected/university/members.html>

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